

uPD17P012

DTS

Operating conditions	
Voltage [V]	4.5 - 5.5
Dhrystone Mips	
Max. Clock [MHz]	
Min. Instr. Time [ns]	
Sub-Clock	
Tmin [°C]	
Tmax [°C]	

Memory	
ROM [KBytes]	8
Type	OTP
RAM [Bytes]	158
EEPROM [Bytes]	0
Instr. Cache [KBytes]	0
Data Cache [KBytes]	

Core	
CPU	17K
Instructions	
Internal Bus [Bits]	4-bit
External Bus [Bits]	
Internal Interrupts [No.]	
External Interrupts [No.]	

Feature	DTS
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Remark	
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Peripherals	
I/O Ports	30
LCD	3x20
FIP	0
ADC	2x6-bit
DAC	2x8-bit
DMA Channels	0
Special Bus	NO
UART	0
Serial Clocked Interface	1
I2C	NO
Timer Channels	3
Watchdog	
PWM	NO
Other Peripherals	

Packages	
Packages types	64 QFP